

Receipt



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re application of: Honeycutt et al, Jeffrey W.

Application No.: 09/651,422

Group No.: 2811

Filed: 08/30/2000

Examiner: Unknown

For: Transistor Structures, Methods of Forming Transistor Structures, and Methods of
Forming Insulative Material Against Conductive Structures

Assistant Commissioner for Patents
Washington, D.C. 20231

TECHNOLOGY CENTER 2800
JUL - 6 2001

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REQUEST FOR CORRECTED FILING RECEIPT

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CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. section 1.8a)

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J. Gaines
Signature

Date: 3/7/01

Jennifer Gaines

(type or print name of person certifying)

(Request for Corrected Filing Receipt--page 1 of 2)

Error in

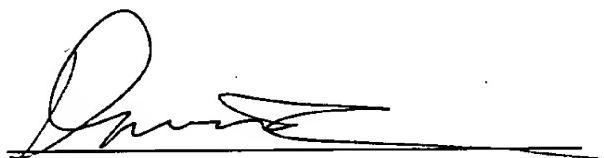
1. Title

Correct data

1. Transistor Structures, Methods of Forming
Transistor Structures, and Methods of
Forming Insulative Material Against
Conductive Structures

3. The correction is not due to any error by applicant and no fee is due.

Date: 3/7/01



David G. Latwesen, Ph.D.
Reg. No. 38,533
Wells, St. John, Roberts, Gregory & Matkin P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201
Tel: 509-624-4276
Customer No. 021567



UNITED STATES PATENT AND TRADEMARK OFFICE

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UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/651,422	08/30/2000	2811	1252	M122-1332	9	31	6

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SUITE 1300
601 W FIRST AVENUE
SPOKANE, WA 992013828



FILING RECEIPT



OC000000005798156

Date Mailed: 02/26/2001

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Applicant(s)

Jeffrey W. Honeycutt, Boise, ID ;
Hassan Shahjamali, Boise, ID ;
Daniel Smith, Boise, ID ;

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Continuing Data as Claimed by Applicant

Foreign Applications

If Required, Foreign Filing License Granted 10/19/2000

Title

↓ Structures

Transistor structures, methods of forming transistor structures, and methods of forming insulative material against conductive structures

Preliminary Class

257

Data entry by : MASON, JAMES

Team : OIPE

Date: 02/26/2001





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CONFIRMATION NO. 9935

Bib Data Sheet

SERIAL NUMBER 09/651,422	FILING DATE 08/30/2000 RULE	CLASS	GROUP ART UNIT 2899	ATTORNEY DOCKET NO. M122-1332
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APPLICANTS

Jeffrey W. Honeycutt, Boise, ID;
 Hassan Shahjamali, Boise, ID;
 Daniel Smith, Boise, ID;

** CONTINUING DATA ***** none 91C

** FOREIGN APPLICATIONS ***** none 94

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/19/2000

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance
Verified and Acknowledged	<i>J. Kennedy Jr.</i> Examiner's Signature Initials
STATE OR COUNTRY ID	SHEETS DRAWING 9
TOTAL CLAIMS 31	INDEPENDENT CLAIMS 6

ADDRESS

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TITLE

Transistor structures, methods of forming transistor structures, and methods of forming insulative material against conductive structures

FILING FEE RECEIVED 1252	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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2nd Request for
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Title still wrong

MI22-1332 (DGL/jg)
Micron Technology Inc.

11/7/00

The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the following papers relating to the Patent application of Honeycutt, et al. Serial No. 09/651,422, filed 8/30/2000, entitled "Transistor Structures, Methods of Forming Transistor Structures, and Methods of Forming Insulative Material Against Conductive Structures".

1. PTO Return Postcard Receipt.
2. Request for Corrected Filing Receipt
3. Copy of Filing Receipt
4. True copy of front page of patent application



Thanks for your
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UNITED STATES PATENT AND TRADEMARK OFFICE



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
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SPOKANE, WA 992013828

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OC000000005489669

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Data entry by : MIDDLETON, MATTIE

Team : OIPE

Date: 10/20/2000



Practitioner's Docket No. MI22-1332



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Honeycutt et al, Jeffrey W.

Application No.: 09/965,142

Group No.: 2811

Filed: 08/30/2000

Examiner: Unknown

For: Transistor Structures, Methods of Forming Transistor Structures, and Methods of Forming Insulative Material Against Conductive Structures

Assistant Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
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REC'D - 5/6/2001
FILED 5/6/2001

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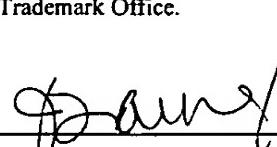
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Jennifer Gaines

(type or print name of person certifying)

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601 West First Ave., Suite 1300
Spokane, WA 99201
509-624-4276
CUSTOMER NO. 021567

EL 465687187



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR LETTERS PATENT

* * * **COPY** * *

**Transistor Structures, Methods of Forming
Transistor Structures, and Methods of Forming
Insulative Material Against Conductive Structures**

* * * * *

INVENTORS

Jeffrey W. Honeycutt
Hassan Shahjamali
Daniel Smith

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ATTORNEY'S DOCKET NO. MI22-1332